

Hardware in the loop relay testing in digital substation environment

Zhou Liu 2017 May 23



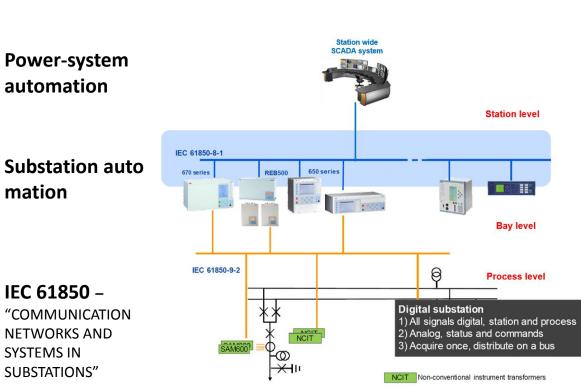




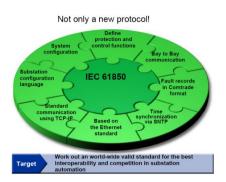
- Background
- Introduction of NTNU protection lab
- Show case



Digital substation



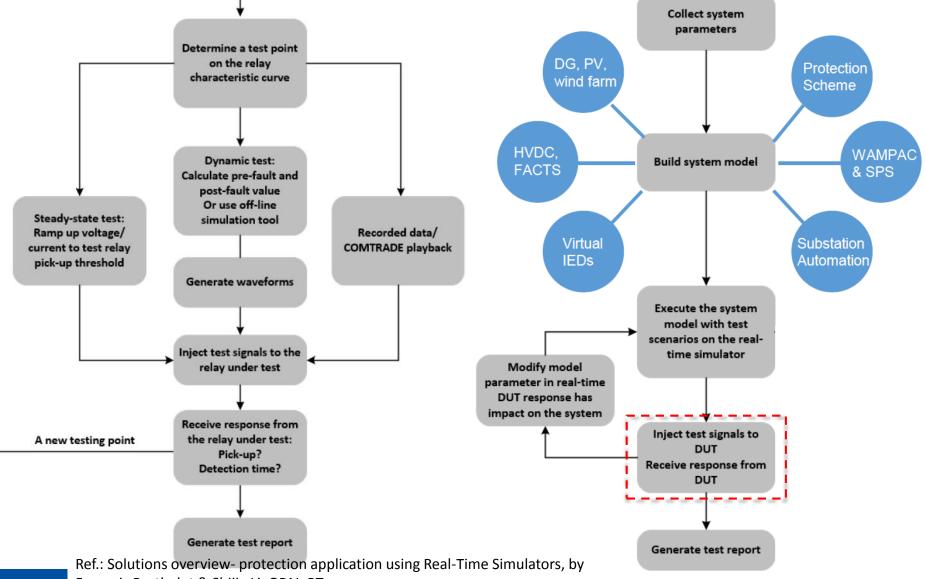
IEC61850—SV and GOOSE



Pros and **cons** of IEC 61850 based digital substation:

- Standard Ethernet-based communications systems
- No "spaghetti" mess behind
- Easy system expansion
- Increased system reliability
- Interoperability
- Increased safety for personnel
- More expensive implementation
- Training required
- Modifications from traditional automation system
- Cyber security

Conventional relay testing VS HIL



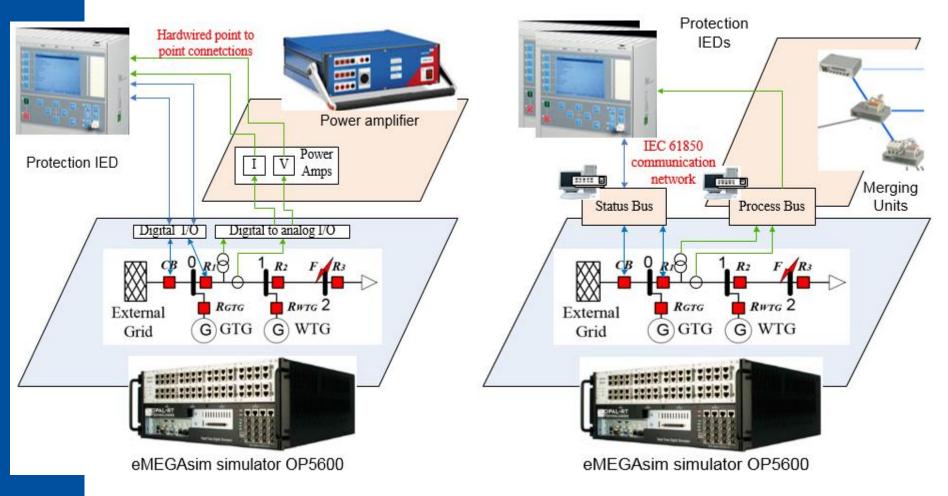
Francois Berthelot & Shijia Li, OPAL-RT.

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Relay testing in HIL way



Electrical Interface VS IEC 61850 interface





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New updates-1. Communication network modelling for HIL protection testing

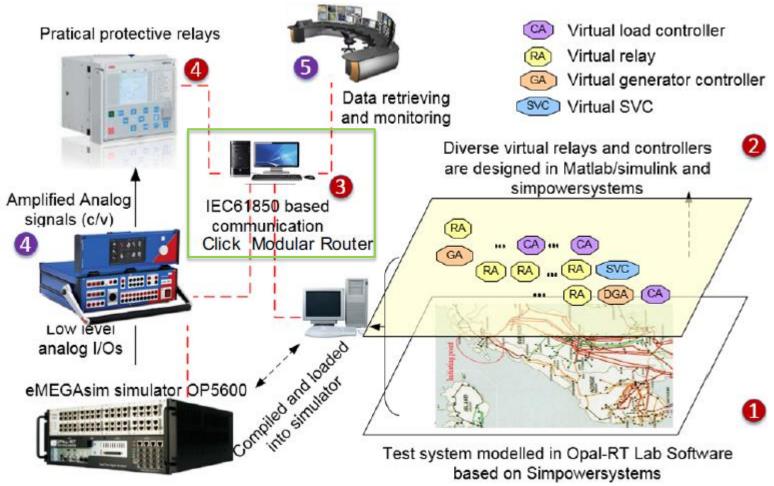
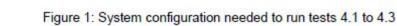


Fig. 1: Network emulator integrated with HIL Setup.

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New updates-2. GPS sync. Spectracom installation NTNU Substation Clock PC with RT-LAB 이미지와 관리되어 한 **GPS** Antenna Ethernet **OPAL-RT Simulator** PCle ON-TIME 1 PPS -----Start of next second in time code UNMODULATED IRIG B003 REFERENCE REFERENCE **IRIG ZERO** B004 IRIG ONE

Spectracom PCIe card



www

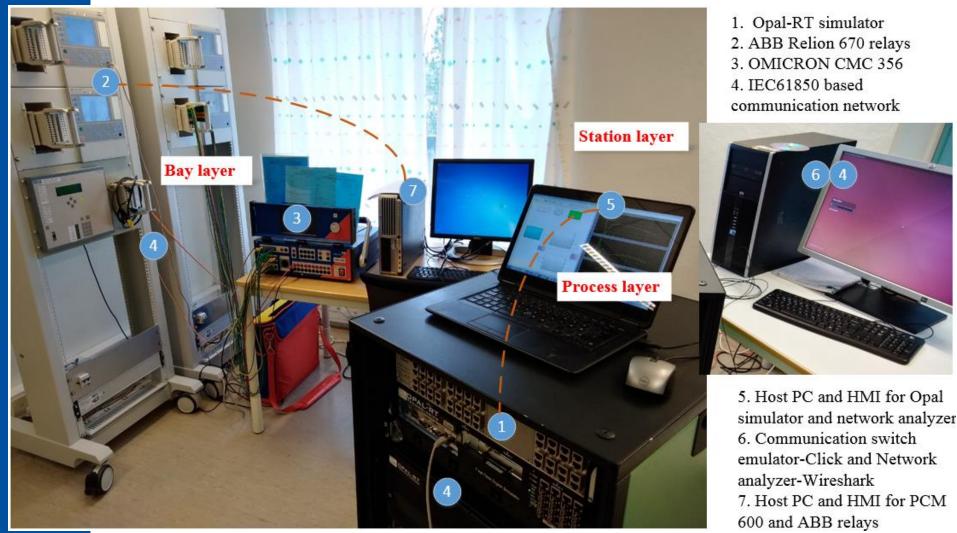
Figure 1: Depiction of both modulated and non-modulated IRIG-B signals.

MODULATED

IRIG

B123 B124

Real time Hardware in the loop (HIL) protection lab at NTNU

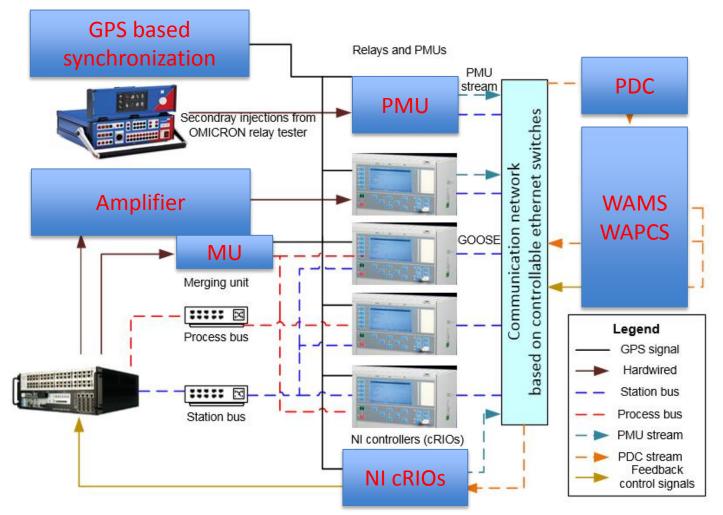


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Perspective and Shortages for the future

Test bed for research and advanced applications—Phase 2&3



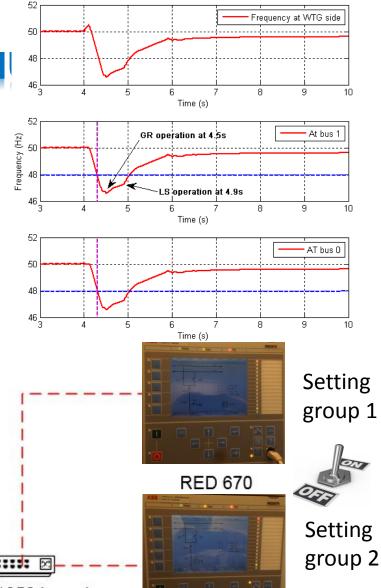




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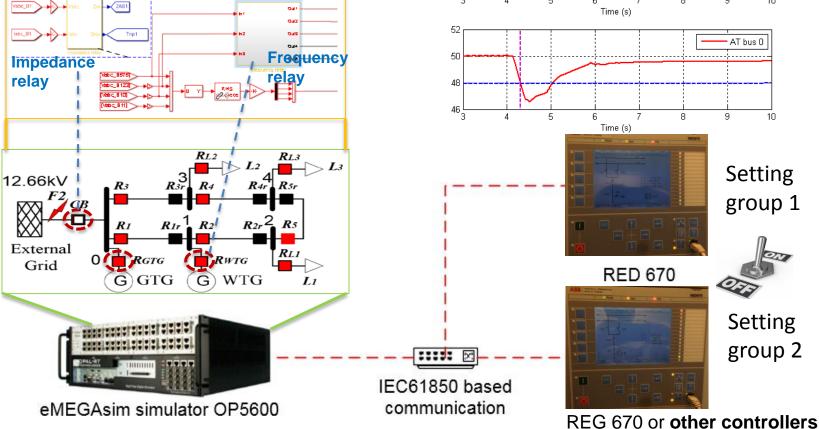


HIL relay testing in NTN



Case 1:

F2 induced islanding and cascading trips



Z. Liu, H. K. Høidalen, M. M. Saha, "A protection and control coordination scheme for distribution network with wind power integration", PACWorld 2016, Ljubljana, Slovenia, 2017.



Thank you for your attention!

