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A lumped model of lymphatic systems suitable for large scale simulations	Giancarlo Storti Gajani, Federica Boschetti, Daniela Negrini, Raffaele Martellaccio, Giorgio Milanese, Federico Bizzarri and Angelo Brambilla
About quantization of audio signals for wildlife intruder detection systems	Lacrimioara Grama and Corneliu Rusu
Analog Computation Methods with the help of analog and pseudo-digital Carry Signals	Christopher Soell, Lan Shi, Andreas Baenisch, Robert Weigel and Thomas Ussmueller
A measurement system for wrist movements in biomedical applications	Bence Borbely, Attila Tihanyi and Peter Szolgay
Efficient global sensitivity analysis method for models of systems with functional outputs	Leszek Opalski
THE ASSESSMENT OF THE STABILITY RESERVE OF LINEAR PERIODICALLY TIME-VARIABLE CIRCUITS IN AN ENVIRONMENT MAOPCs	Yuriy Shapovalov, Bohdan Mandziy and Dariya Bachyk
A Wide Range All-Digital Delay Locked Loop for Video Applications	Muhammad Pasha, Yasir Ali Shah and Jacob Wikner
Adaptive Sparse Matrix Indexing Technique for Simulation of Electronic Circuits Based on λ-calculus	David Cerny and Josef Dobes
4 Sub-/Near-Threshold Flip-Flops with Application to Frequency Dividers	Ali Asghar Vatanjou, Trond Ytterdal and Snorre Aunet
Generalized Division-Free Architecture and Compact Memory Structure for Resampling in Particle Filters	Syed Asad Alam and Oscar Gustafsson
Application Possibilities of VDCC In General Floating Element Simulator Circuit	Aslihan Kartci, Umut Engin Ayten, Norbert Herencsar, Roman Sotner, Jan Jerabek and Kamil Vrba
Distant Aircraft Detection in Sense-and-Avoid on Kilo-Processor Architectures	Tamas Zsedrovits, Akos Zarandy, Borbala Jani Matyasne Pencz, Antal Hiba, Mate Nemeth and Balint Vanek

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Grounded Inductance Simulator Topologies Realization with Single Current Differencing Current Conveyor	Fırat Kacar, Hakan Kuntman and Ayten Kuntman
Quick and Easy CMOS Amplifier Design And Optimization	Gordon Roberts
New Image Denoising Method using Multiple-Minimum Cuts based on \\ Maximum-Flow Neural Network	Masatoshi Sato, Hisashi Aomori, Tsuyoshi Otake and Mamoru Tanaka
Memristor-based Center-of-Gravity (COG) Defuzzifier Circuit	Sherif Amer, Hassanein Amer, Ahmed Madian and Ahmed Emara
Loop Filter Design and Optimization for Quadrature Delta-Sigma Converters	Marko Neitola
Thermal Load Analysis and Real Time Hot Spots Recognition in TOKAMAKs: a Cellular Nonlinear Networks approach	Maria Laura Apicella, Fabio Battaglia, Arturo Buscarino, Claudia Corradino, Luigi Fortuna, Mattia Frasca and Giuseppe Mazzitelli
A Novel Forward Body Biasing Technique for Subthreshold Ring Oscillators	Yuan Chang, Shailesh Singh Chouhan and Kari Halonen
Electrically Testable CMOS Image Pixel Circuit	Masaki Hashizume, Shingo Saijyo and Hiroyuki Yotsuyanagi
Cellular Network of Networks on Dynamically Partial Reconfigurable FPGA	Emrah Abtioglu, Ramazan Yeniceri and Mustak Erhan Yalcin
Multiconductor Transmission Line Model with Frequency Dependent Parameters in Time Domain	Agnieszka Wardzińska and Wojciech Bandurski
Surfing Front-end Architectures for Ultrasound Imaging Systems	Peng Wang, Thomas Halvorsrød and Trond Ytterdal
Power-Efficient Time-to-Digital Converter for All-Digital Frequency Locked Loops	Muhammad Pasha, Niklas Andersson and Mark Vesterbacka